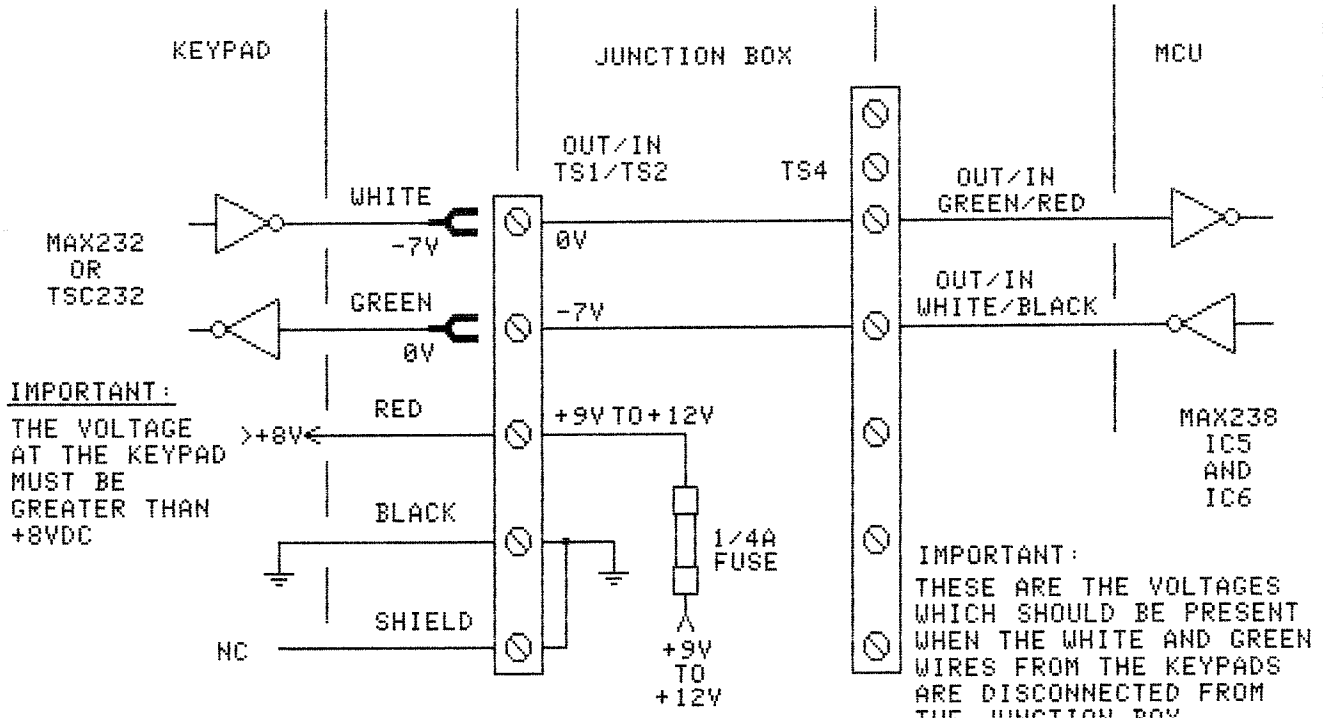


KEYPAD TO JUNCTION BOX DC VOLTAGES



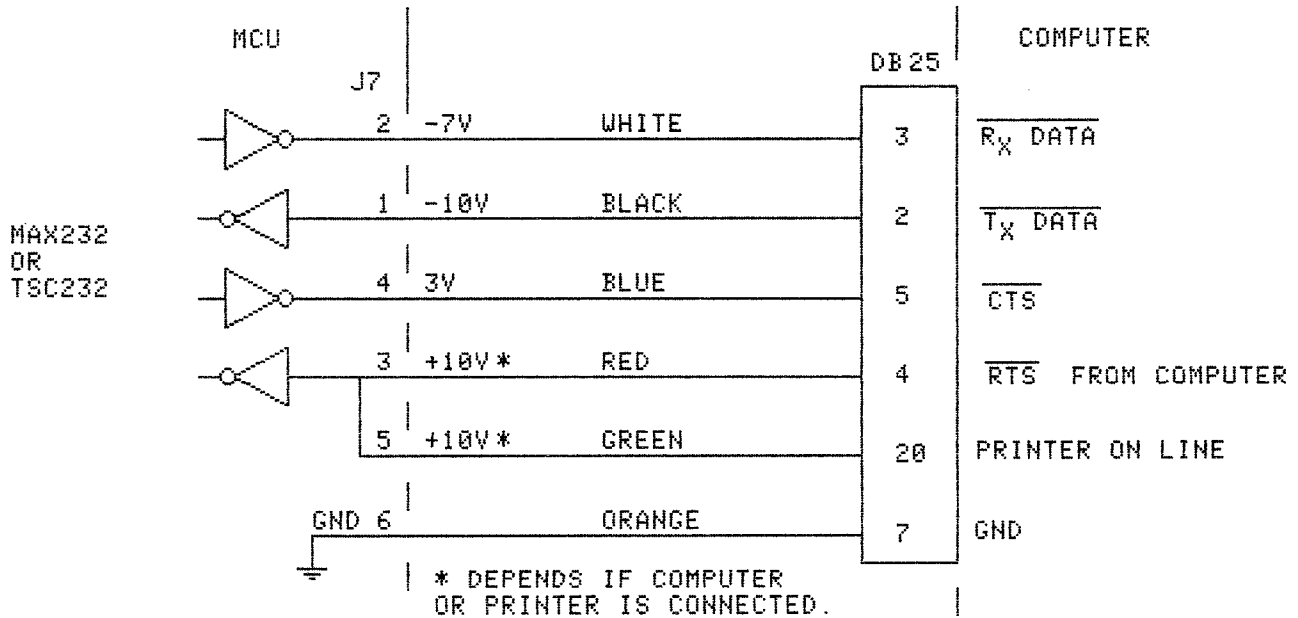
IMPORTANT:
THE VOLTAGE AT THE KEYPAD MUST BE GREATER THAN +8VDC

IMPORTANT:
THESE ARE THE VOLTAGES WHICH SHOULD BE PRESENT WHEN THE WHITE AND GREEN WIRES FROM THE KEYPADS ARE DISCONNECTED FROM THE JUNCTION BOX.

REFER TO FIG. 3.7 FOR COMPLETE JUNCTION BOX SCHEMATIC.

NOTE: DRIVERS AND RECEIVERS.

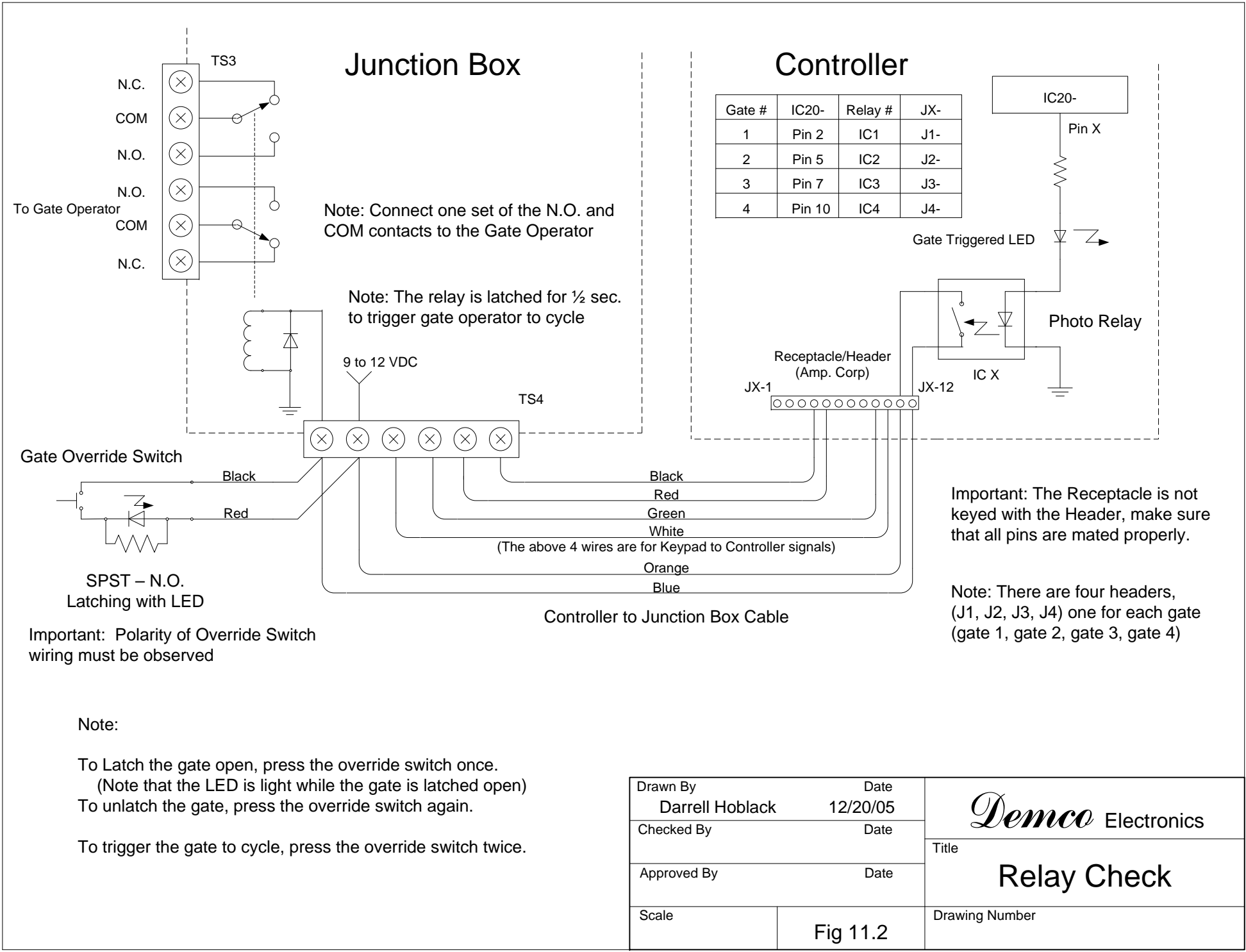
MCU TO COMPUTER DC VOLTAGES



* DEPENDS IF COMPUTER OR PRINTER IS CONNECTED.

NOTE: THESE VOLTAGES ARE MEASURED IN THE MCU AT J7 (BOTTOM RIGHT CORNER) WITH THE SERIAL CABLE CONNECTED TO THE FACILITY COMPUTER.

DRAWN BY DARRELL HOBLACK		DATE 9/23/91	DEMCO ELECTRONICS
CHECKED BY		DATE	
APPROVED		DATE	TITLE DC INTERFACE VOLTAGES
SCALE	FIG 11.1		DRAWING NUMBER



Junction Box

Controller

Gate #	IC20-	Relay #	JX-
1	Pin 2	IC1	J1-
2	Pin 5	IC2	J2-
3	Pin 7	IC3	J3-
4	Pin 10	IC4	J4-

Note: Connect one set of the N.O. and COM contacts to the Gate Operator

Note: The relay is latched for 1/2 sec. to trigger gate operator to cycle

Important: The Receptacle is not keyed with the Header, make sure that all pins are mated properly.

Note: There are four headers, (J1, J2, J3, J4) one for each gate (gate 1, gate 2, gate 3, gate 4)

Gate Override Switch

SPST – N.O.
Latching with LED

Important: Polarity of Override Switch wiring must be observed

Note:

To Latch the gate open, press the override switch once.
(Note that the LED is light while the gate is latched open)
To unlatch the gate, press the override switch again.

To trigger the gate to cycle, press the override switch twice.

Controller to Junction Box Cable

Drawn By Darrell Hoblack	Date 12/20/05	
Checked By	Date	
Approved By	Date	
Scale	Fig 11.2	Title Relay Check
		Drawing Number